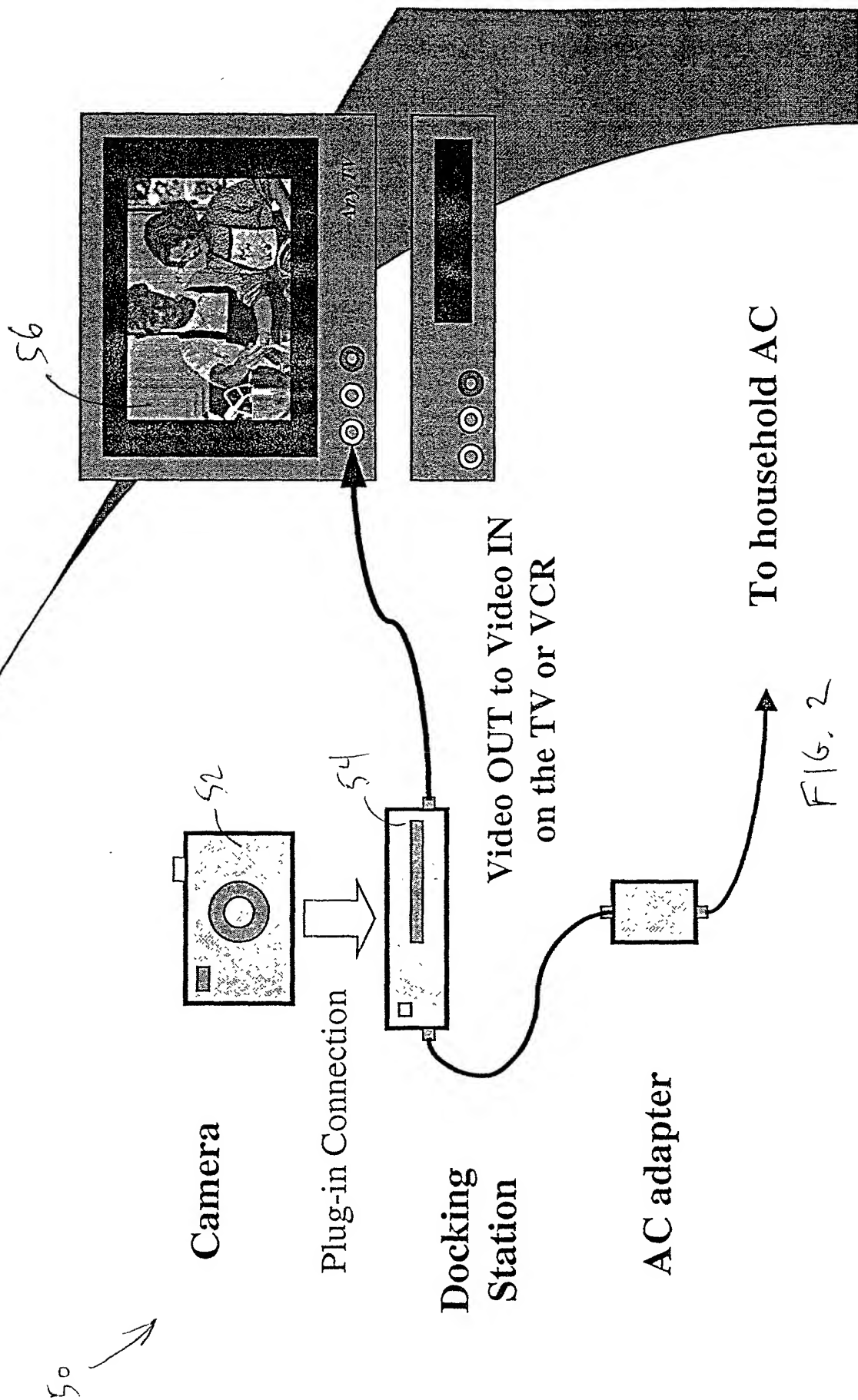


SVI Docking Station

FIG. 1



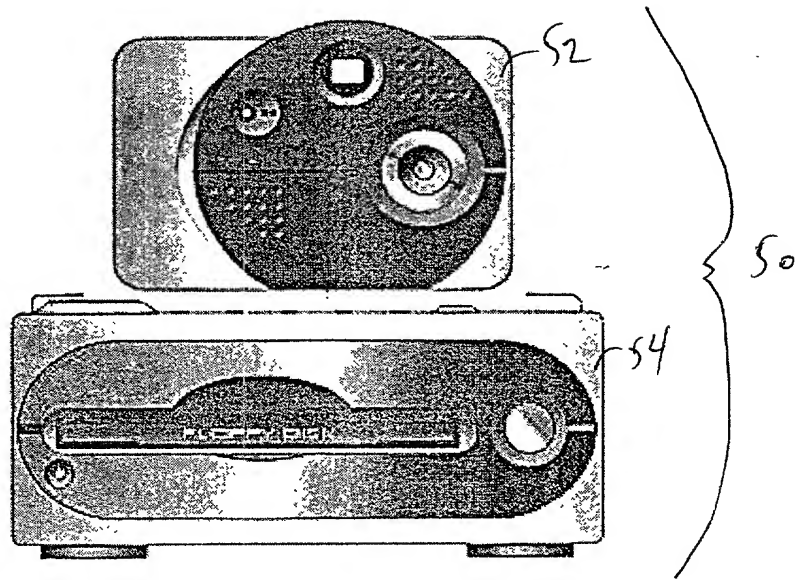


FIG. 3

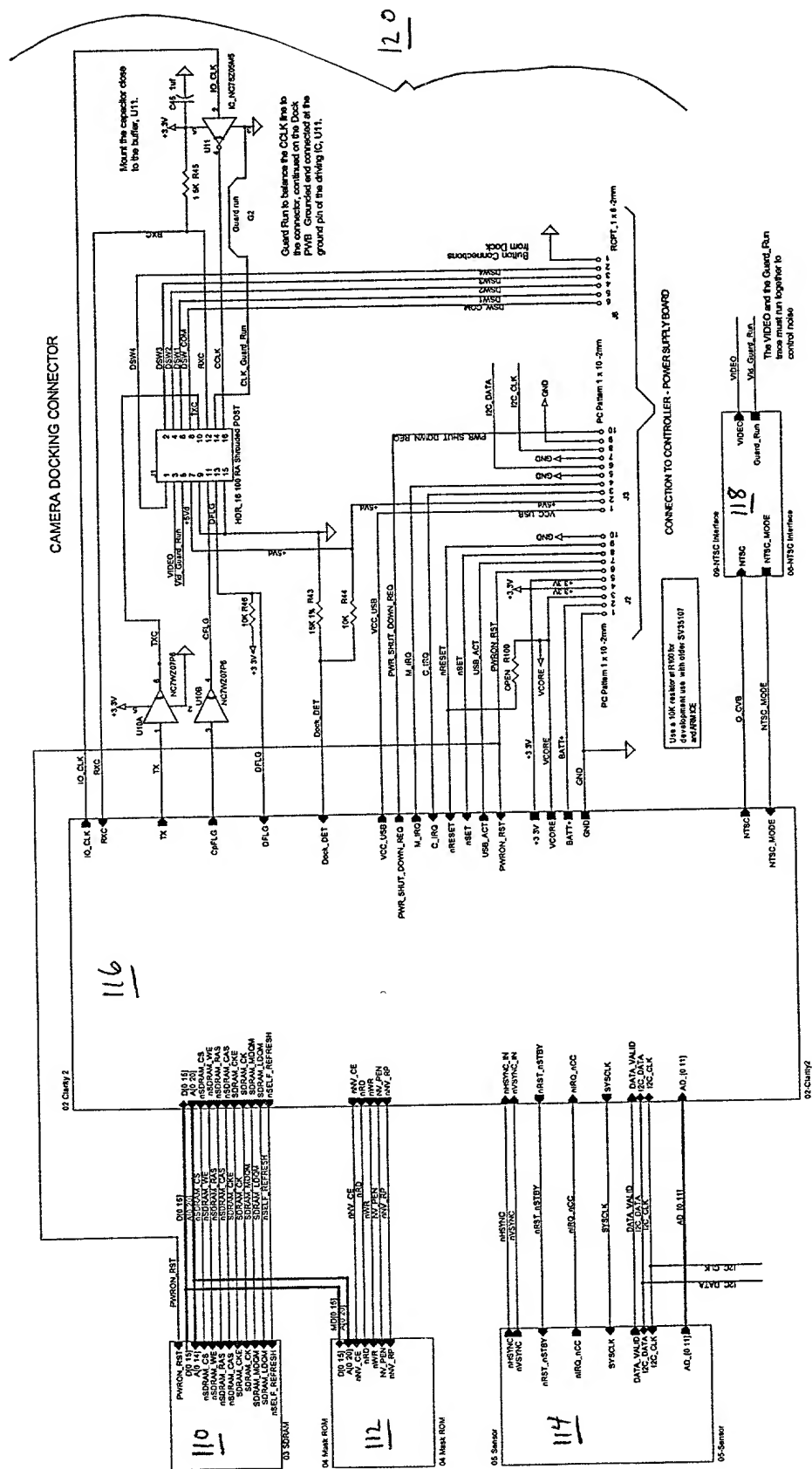
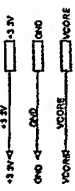
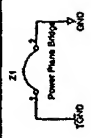


Fig. 4

NOTE: TOND is a portion of the ground plane projected with out. It is not a separate unit!



# sound vision

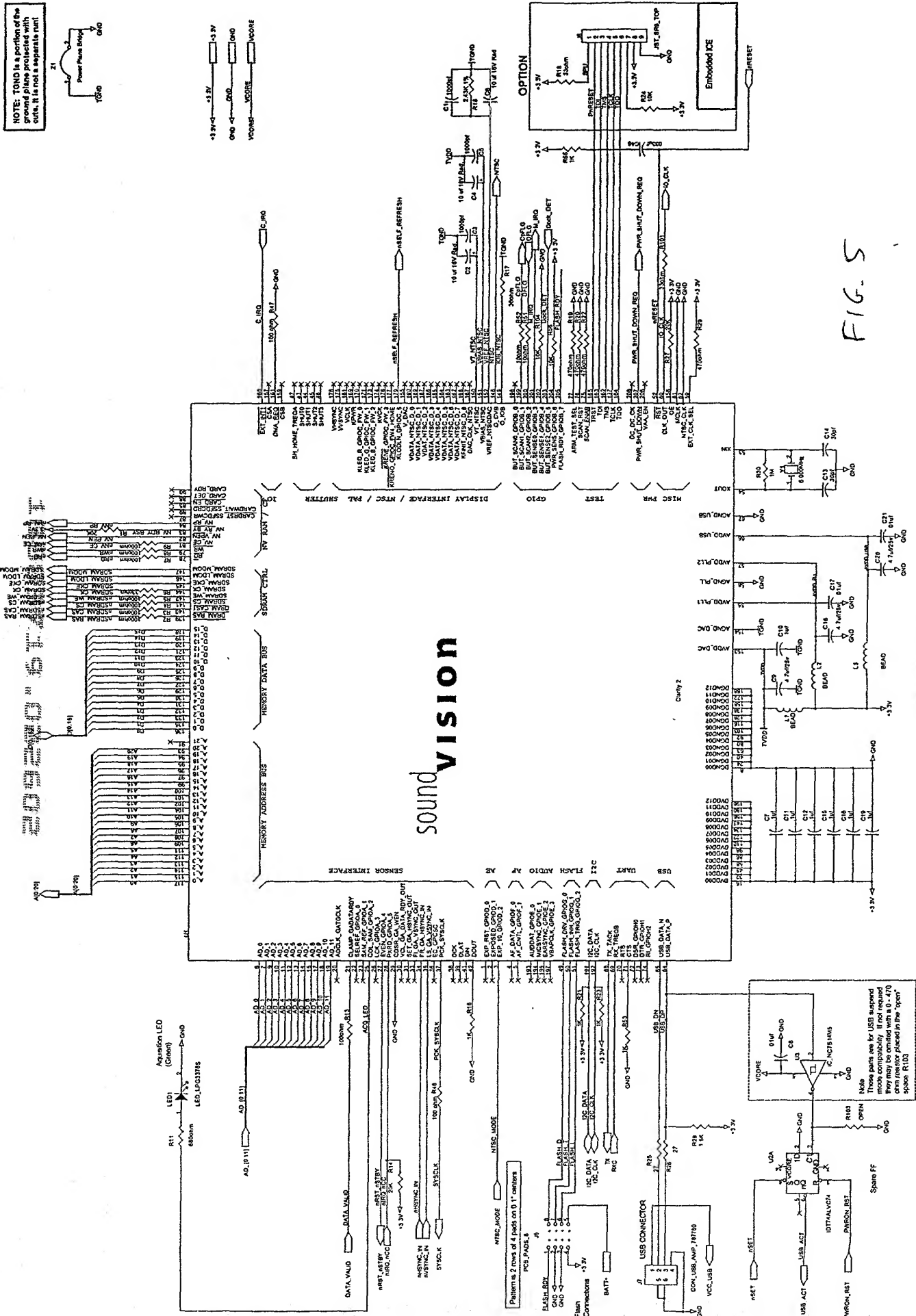
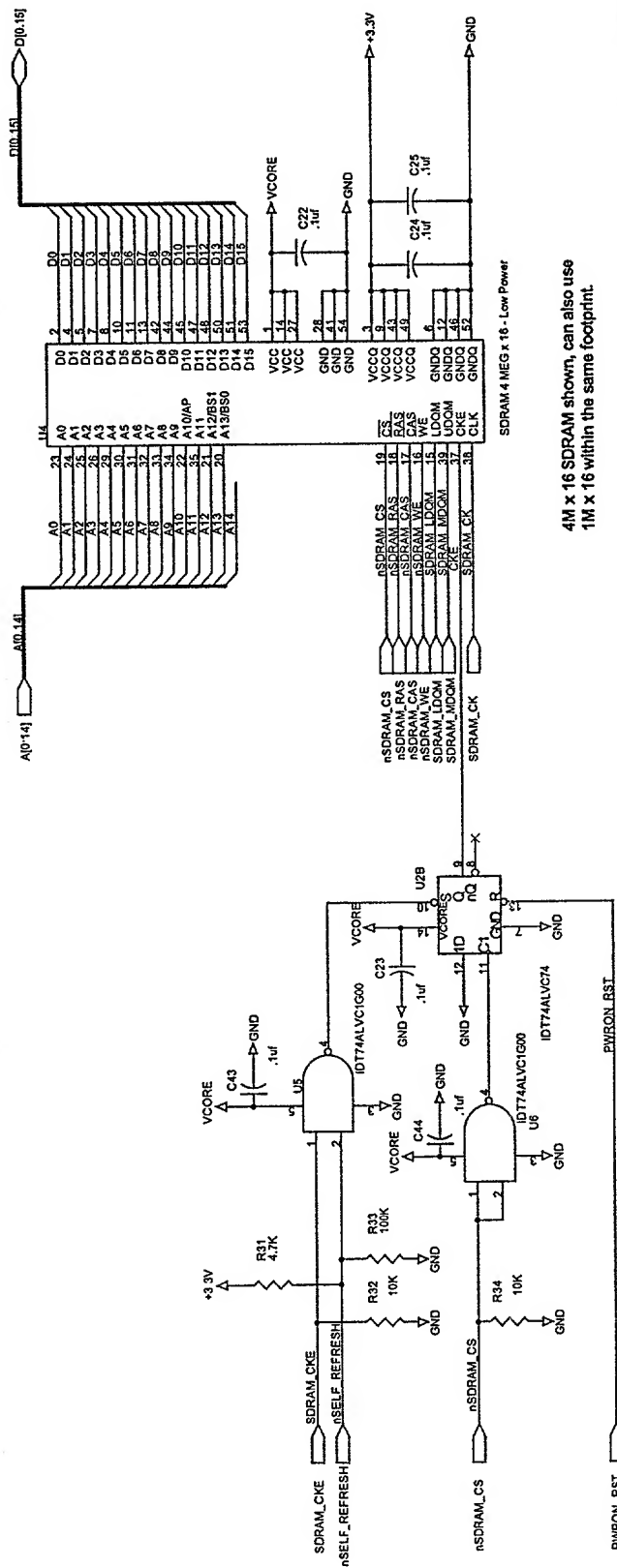


FIG. 5

Place the 0.1uF and 0.01uF capacitors close to the Clarity pins



**NOTE:** This design requires SDRAM parts with isolated Vcc and VccQ on chip.

\*\*\*\*\* NOTE: \*\*\*\*\*

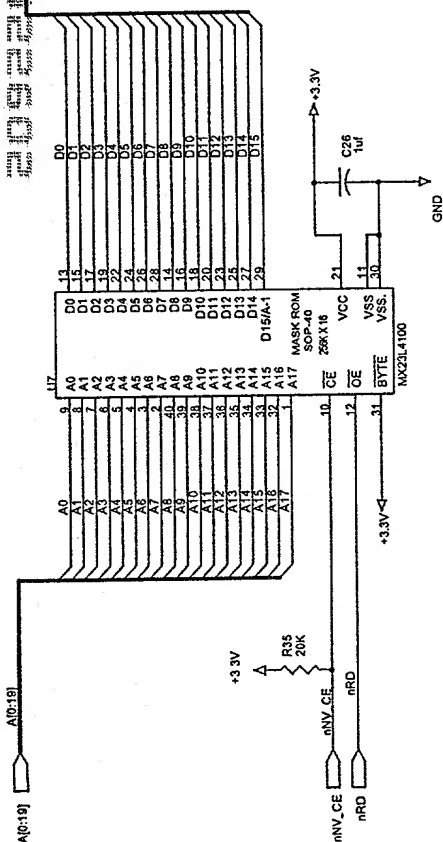
Low power SDRAM should be used. Power consumption when the camera is shut down depends on the SDRAM consumption in Self Refresh Mode as the major component of quiescent consumption.

**4M x 16 SDRAM shown, can also use 1M x 16 within the same footprint.**

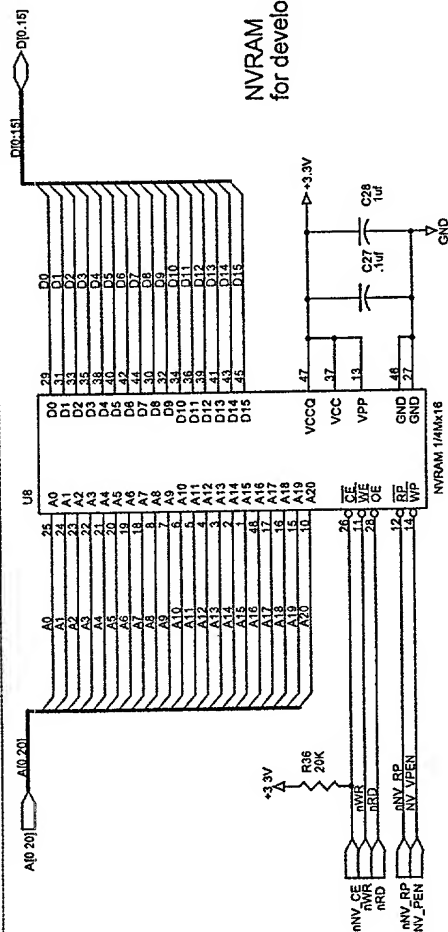
169

5250  
 5251  
 5252  
 5253  
 5254  
 5255  
 5256  
 5257  
 5258  
 5259  
 5260  
 5261  
 5262  
 5263  
 5264  
 5265  
 5266  
 5267  
 5268  
 5269  
 5270  
 5271  
 5272  
 5273  
 5274  
 5275  
 5276  
 5277  
 5278  
 5279  
 5280  
 5281  
 5282  
 5283  
 5284  
 5285  
 5286  
 5287  
 5288  
 5289  
 5290  
 5291  
 5292  
 5293  
 5294  
 5295  
 5296  
 5297  
 5298  
 5299  
 5300  
 5301  
 5302  
 5303  
 5304  
 5305  
 5306  
 5307  
 5308  
 5309  
 5310  
 5311  
 5312  
 5313  
 5314  
 5315  
 5316  
 5317  
 5318  
 5319  
 5320  
 5321  
 5322  
 5323  
 5324  
 5325  
 5326  
 5327  
 5328  
 5329  
 5330  
 5331  
 5332  
 5333  
 5334  
 5335  
 5336  
 5337  
 5338  
 5339  
 5340  
 5341  
 5342  
 5343  
 5344  
 5345  
 5346  
 5347  
 5348  
 5349  
 5350  
 5351  
 5352  
 5353  
 5354  
 5355  
 5356  
 5357  
 5358  
 5359  
 5360  
 5361  
 5362  
 5363  
 5364  
 5365  
 5366  
 5367  
 5368  
 5369  
 5370  
 5371  
 5372  
 5373  
 5374  
 5375  
 5376  
 5377  
 5378  
 5379  
 5380  
 5381  
 5382  
 5383  
 5384  
 5385  
 5386  
 5387  
 5388  
 5389  
 5390  
 5391  
 5392  
 5393  
 5394  
 5395  
 5396  
 5397  
 5398  
 5399  
 5400  
 5401  
 5402  
 5403  
 5404  
 5405  
 5406  
 5407  
 5408  
 5409  
 5410  
 5411  
 5412  
 5413  
 5414  
 5415  
 5416  
 5417  
 5418  
 5419  
 5420  
 5421  
 5422  
 5423  
 5424  
 5425  
 5426  
 5427  
 5428  
 5429  
 5430  
 5431  
 5432  
 5433  
 5434  
 5435  
 5436  
 5437  
 5438  
 5439  
 5440  
 5441  
 5442  
 5443  
 5444  
 5445  
 5446  
 5447  
 5448  
 5449  
 5450  
 5451  
 5452  
 5453  
 5454  
 5455  
 5456  
 5457  
 5458  
 5459  
 5460  
 5461  
 5462  
 5463  
 5464  
 5465  
 5466  
 5467  
 5468  
 5469  
 5470  
 5471  
 5472  
 5473  
 5474  
 5475  
 5476  
 5477  
 5478  
 5479  
 5480  
 5481  
 5482  
 5483  
 5484  
 5485  
 5486  
 5487  
 5488  
 5489  
 5490  
 5491  
 5492  
 5493  
 5494  
 5495  
 5496  
 5497  
 5498  
 5499  
 5500  
 5501  
 5502  
 5503  
 5504  
 5505  
 5506  
 5507  
 5508  
 5509  
 5510  
 5511  
 5512  
 5513  
 5514  
 5515  
 5516  
 5517  
 5518  
 5519  
 5520  
 5521  
 5522  
 5523  
 5524  
 5525  
 5526  
 5527  
 5528  
 5529  
 5530  
 5531  
 5532  
 5533  
 5534  
 5535  
 5536  
 5537  
 5538  
 5539  
 5540  
 5541  
 5542  
 5543  
 5544  
 5545  
 5546  
 5547  
 5548  
 5549  
 5550  
 5551  
 5552  
 5553  
 5554  
 5555  
 5556  
 5557  
 5558  
 5559  
 5560  
 5561  
 5562  
 5563  
 5564  
 5565  
 5566  
 5567  
 5568  
 5569  
 5570  
 5571  
 5572  
 5573  
 5574  
 5575  
 5576  
 5577  
 5578  
 5579  
 5580  
 5581  
 5582  
 5583  
 5584  
 5585  
 5586  
 5587  
 5588  
 5589  
 5590  
 5591  
 5592  
 5593  
 5594  
 5595  
 5596  
 5597  
 5598  
 5599  
 5600  
 5601  
 5602  
 5603  
 5604  
 5605  
 5606  
 5607  
 5608  
 5609  
 5610  
 5611  
 5612  
 5613  
 5614  
 5615  
 5616  
 5617  
 5618  
 5619  
 5620  
 5621  
 5622  
 5623  
 5624  
 5625  
 5626  
 5627  
 5628  
 5629  
 5630  
 5631  
 5632  
 5633  
 5634  
 5635  
 5636  
 5637  
 5638  
 5639  
 5640  
 5641  
 5642  
 5643  
 5644  
 5645  
 5646  
 5647  
 5648  
 5649  
 5650  
 5651  
 5652  
 5653  
 5654  
 5655  
 5656  
 5657  
 5658  
 5659  
 5660  
 5661  
 5662  
 5663  
 5664  
 5665  
 5666  
 5667  
 5668  
 5669  
 5670  
 5671  
 5672  
 5673  
 5674  
 5675  
 5676  
 5677  
 5678  
 5679  
 5680  
 5681  
 5682  
 5683  
 5684  
 5685  
 5686  
 5687  
 5688  
 5689  
 5690  
 5691  
 5692  
 5693  
 5694  
 5695  
 5696  
 5697  
 5698  
 5699  
 5700  
 5701  
 5702  
 5703  
 5704

- NOTES:
1. Address lines on NVRAM limited to match 160 pin Clarity 4.
  2. Flash NVRAM is for prototype use only.
  3. Mask ROM pinout matches similar Oki MSM534032E-SOP-40.
  4. Do not duplicate the 20K resistor



**MASK ROM**



## NVRAM for development

F16.7

51230001

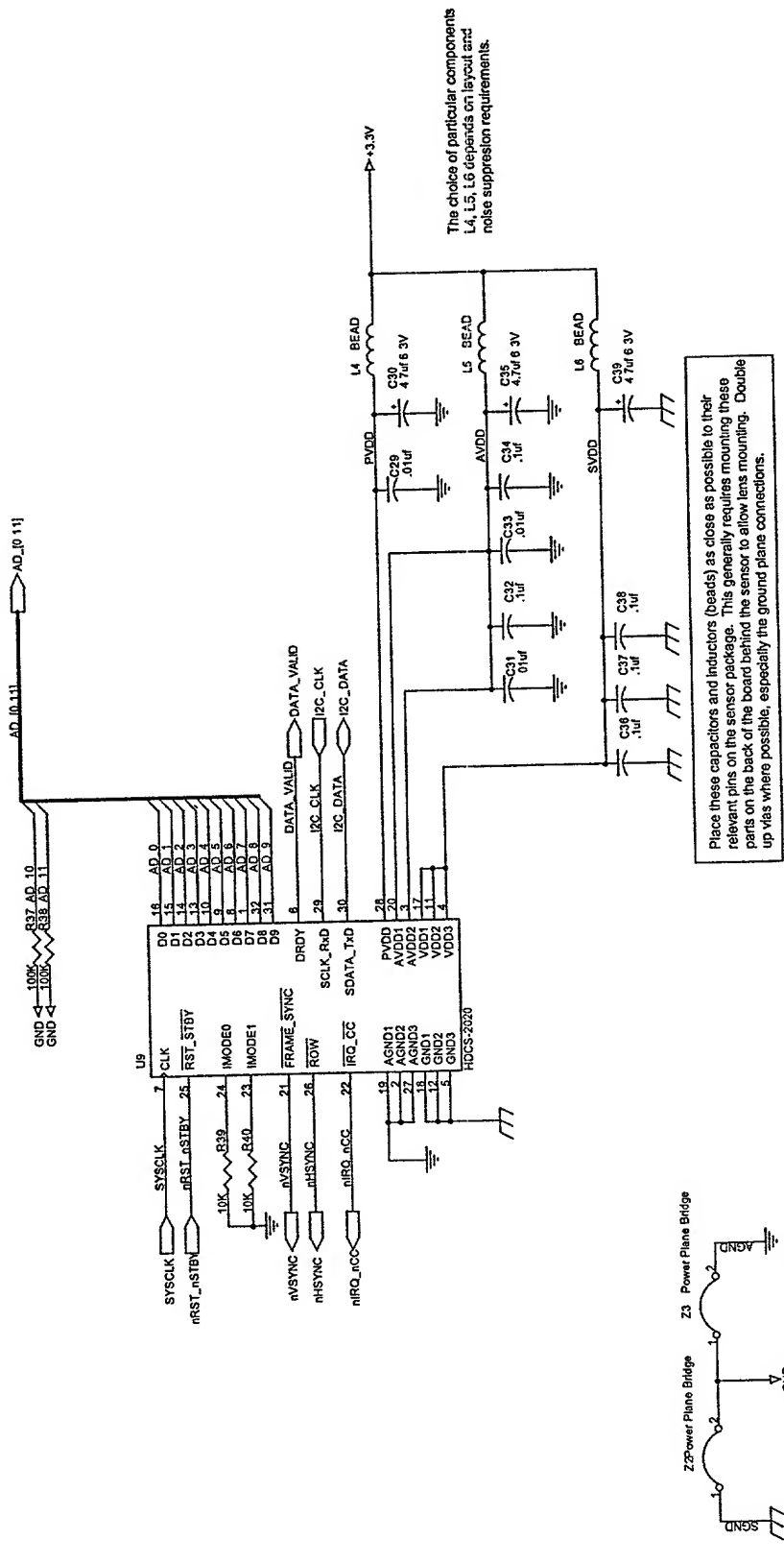
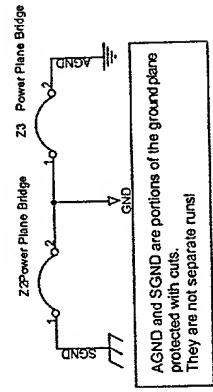


FIG. 8





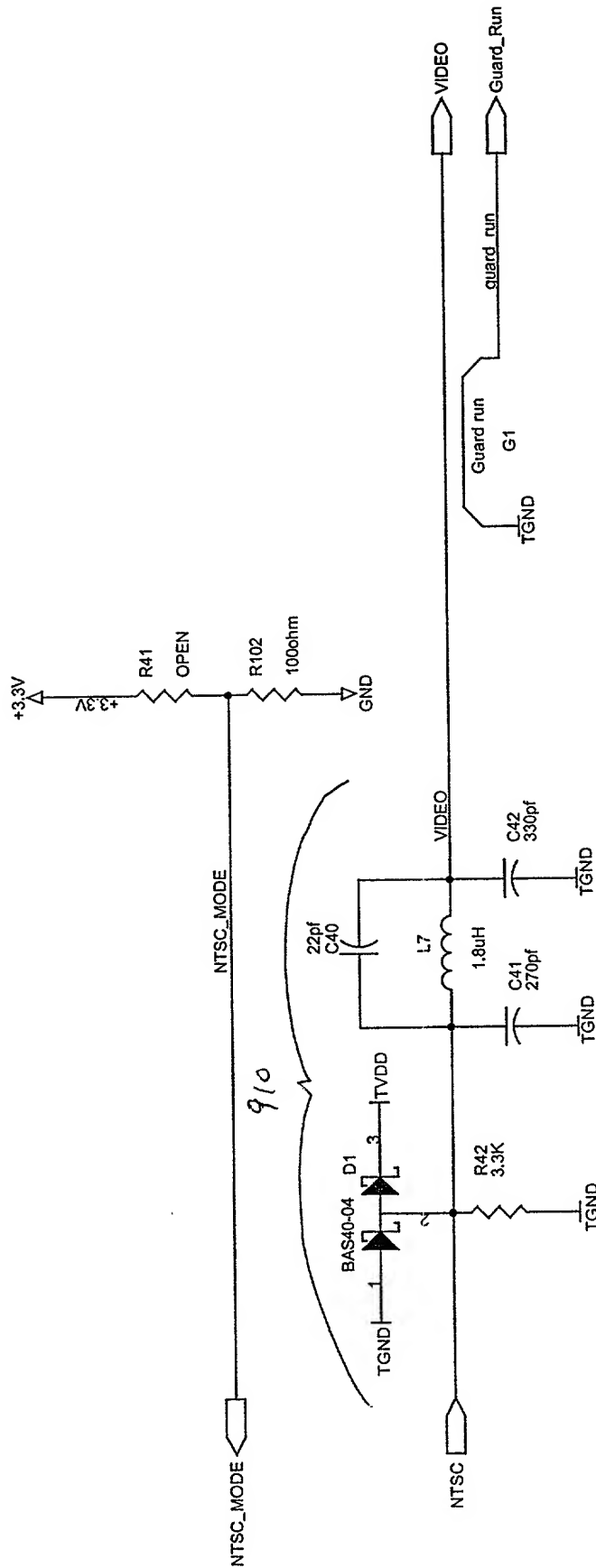
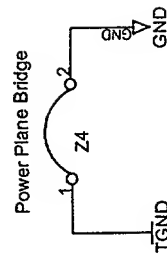
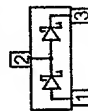


FIG. 9



TGND is a portion of the ground plane protected with cuts. See Clarity sheet. It is not a separate run!

NOTE:  
The diode pin numbering shown here is non-standard. Make sure the PCB layout matches the diode(s) used in your design.



### MAIN BOARD CONNECTIONS

**J2**

Pin	Signal
1	BATT+
2	VDDC
3	VDDC
4	+3.3V
5	
6	PMURON RST
7	US1 AC1
8	US1 AC1
9	PRESET
10	PC Pattern 1x10-2mm

**J3**

Pin	Signal
1	VCC USB
2	NC
3	C1RQ
4	M1RQ
5	
6	I2C DATA
7	I2C CLK
8	
9	PWR SHUT DOWN REQ
10	PC Pattern 1x10-2mm

**J8**

Pin	Signal
1	DSW4
2	DSW3
3	DSW2
4	DSW1
5	DSW1 COM
6	

PCPT\_1x6-2mm



**NOTE:**  
USE VERY WIDE TRACES FOR BATT+, +5Vd,  
VCORE, VBB and the +3.3V POWER PATH,  
PREFERABLY ON A POWER PLANE





✓ 1310

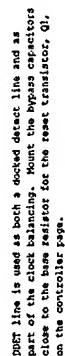


FIG. 13

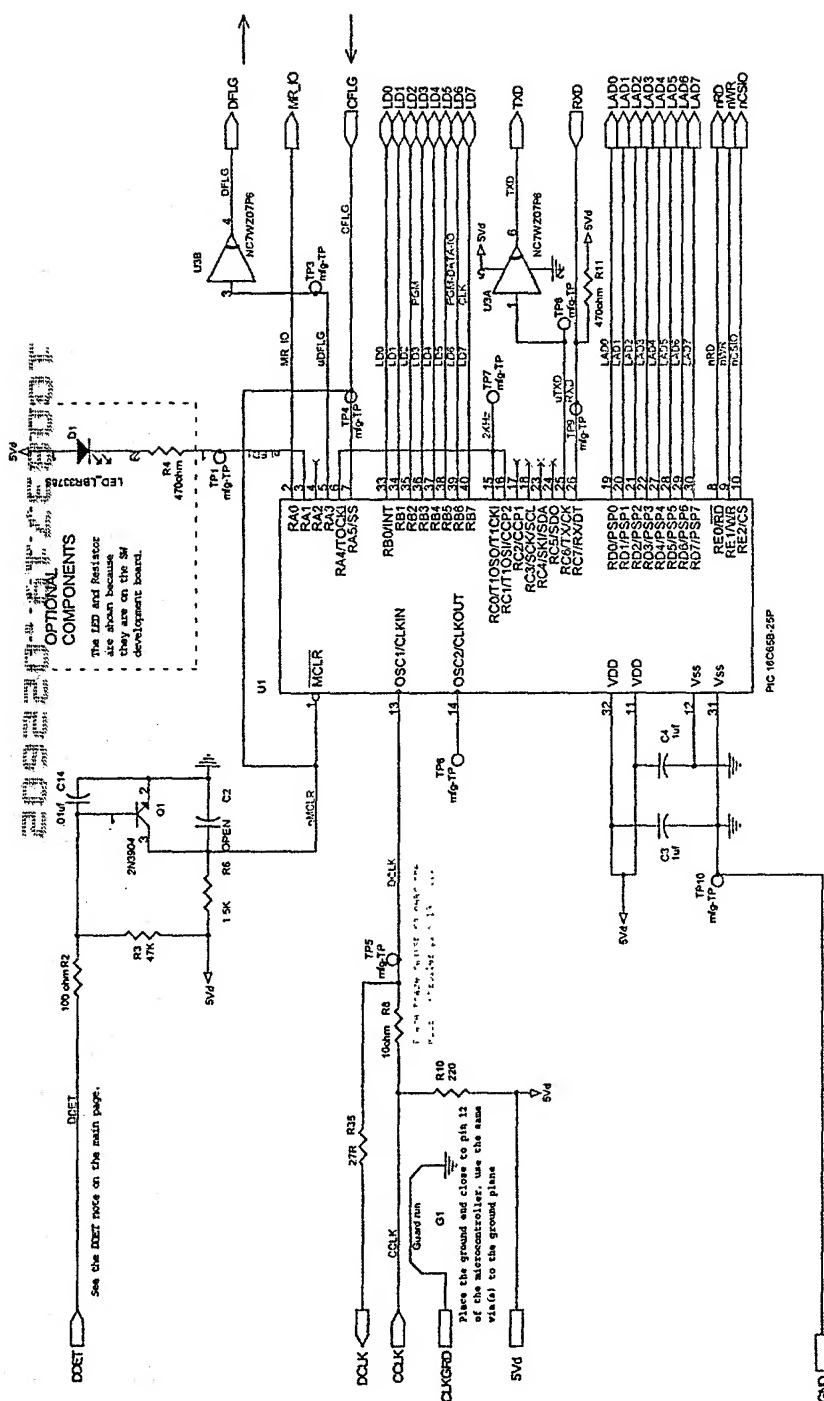


FIG. 14



